

**Amendments to the Specification**

**On page 3, line 10 of the substitute specification filed on April 26, 2007,  
please add the following paragraph:**

Fig. 3 shows a block diagram of the memory system.

**On page 6, line 12 of the substitute specification filed on April 26, 2007,  
please add the following paragraph:**

In Fig. 3, a block diagram of a nonvolatile memory system is shown. The memory controller CTR exchanges data and commands via the host bus HB with a host system. The data is exchanged with the nonvolatile memory chips NVM via the data bus DB. In the memory controller CTR the functional blocks program memory PM, processing unit PROC and the internal flag memory FM, which is volatile RAM, are included. The memory controller CTR executes, on demand of the host system, all read, write and erase operations on the nonvolatile memory NVM. Therefore it translates logical addresses of the host system to physical addresses in the nonvolatile memory NVM. To do this, an address allocation table BZT is built up in the nonvolatile memory, showing the physical block addresses SBA allocated to the logical block addresses LBA. In the flag memory FM an intermediate address table ZZT is built up, with updated physical block addresses SBA to some logical block addresses LBA. To reconstruct the allocation tables, a reconstruction table RKT is continuously updated in the nonvolatile memory NVM.

**On page 8, lines 1-14 of the substitute specification filed on April 26,**

**2007, please amend as follows:**

~~Reference~~References<sup>1</sup> Numerals

ABA	Alternate block address
BZT	Block pointer table
<del>CTR</del>	<del>Memory controller</del>
<del>DB</del>	<del>Data bus</del>
FE	Completion entry
<del>FM</del>	<del>Flag memory</del>
FZ	Completion counter
<del>HB</del>	<del>Host bus</del>
LBA	Logical block address
LBA <sub>n</sub>	Logical block address n in the ZZT
<del>NVM</del>	<del>Nonvolatile memory</del>
OE	Reorganization entry
<del>PM</del>	<del>Program memory</del>
<del>PROC</del>	<del>Processing Unit</del>
RE	Reconstruction entry
RKT	Reconstruction table
SBA	Memory block address
VD	Administrative record data
ZZT	Intermediate Pointer Table

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<sup>1</sup> Translator's Note: Approximately half of this word is missing in the German-language document on which this translation is based. It appears however, that the German word is "Bezugszeichen", which was translated as "Reference Numerals".